

Fig 1

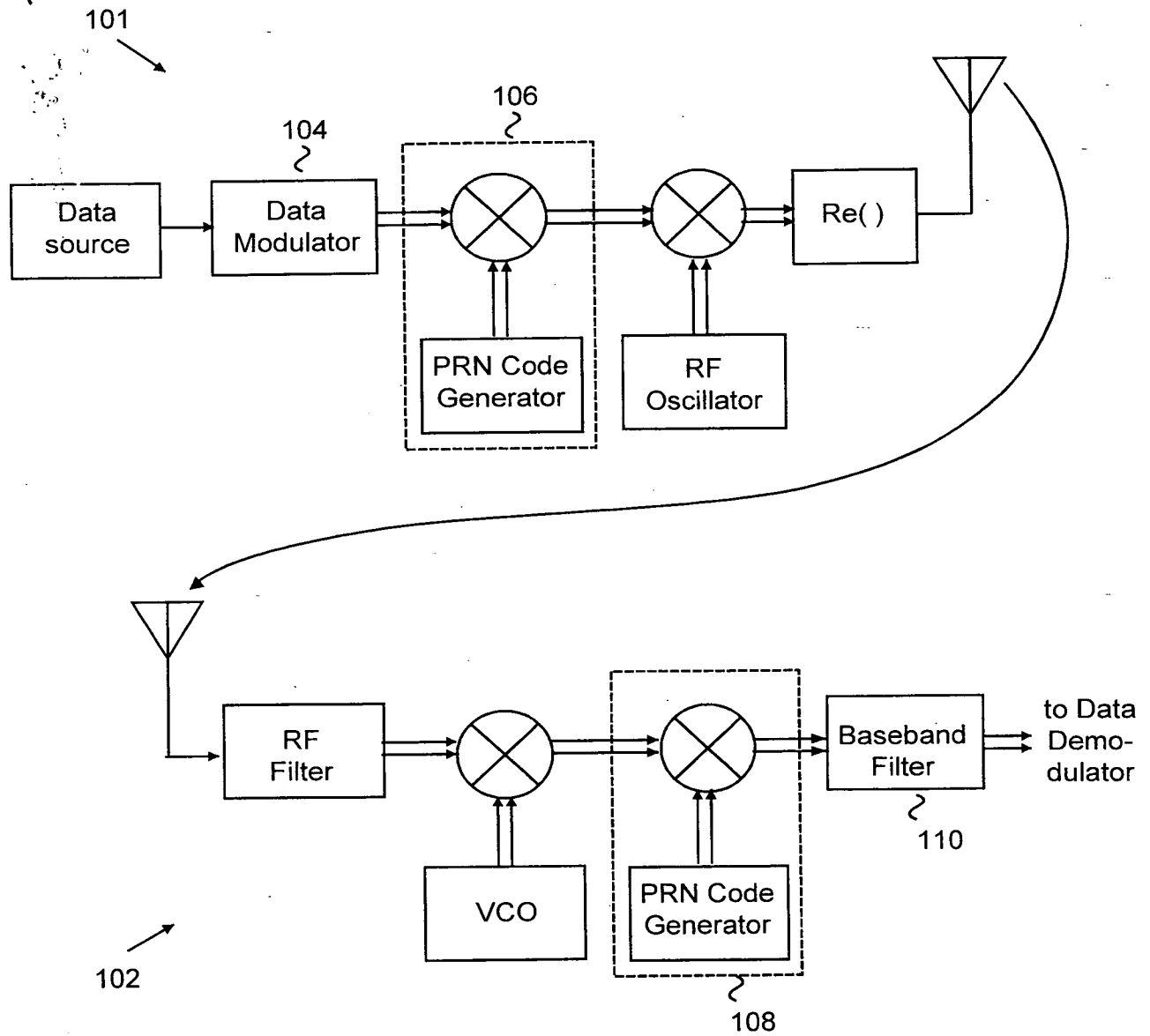


Fig 2

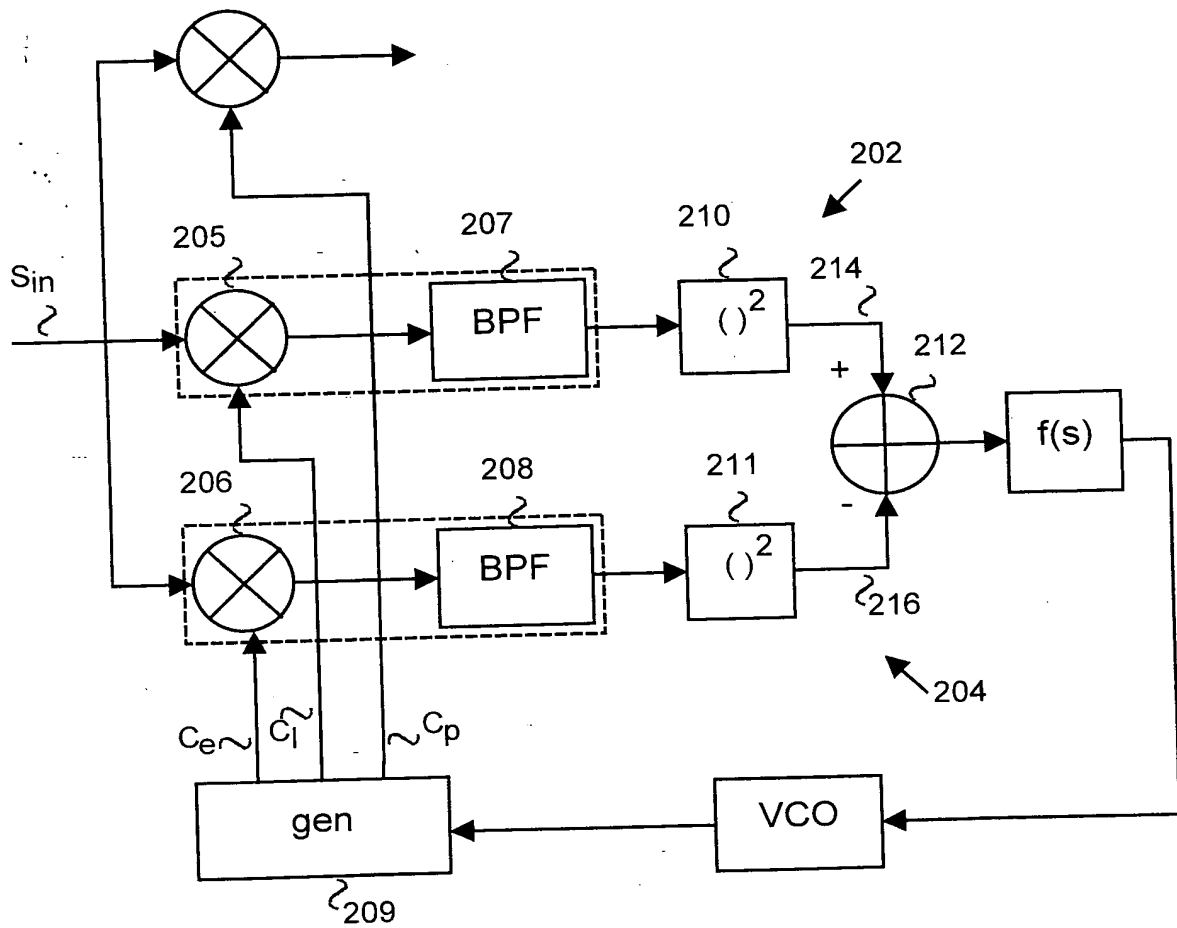


Fig 3

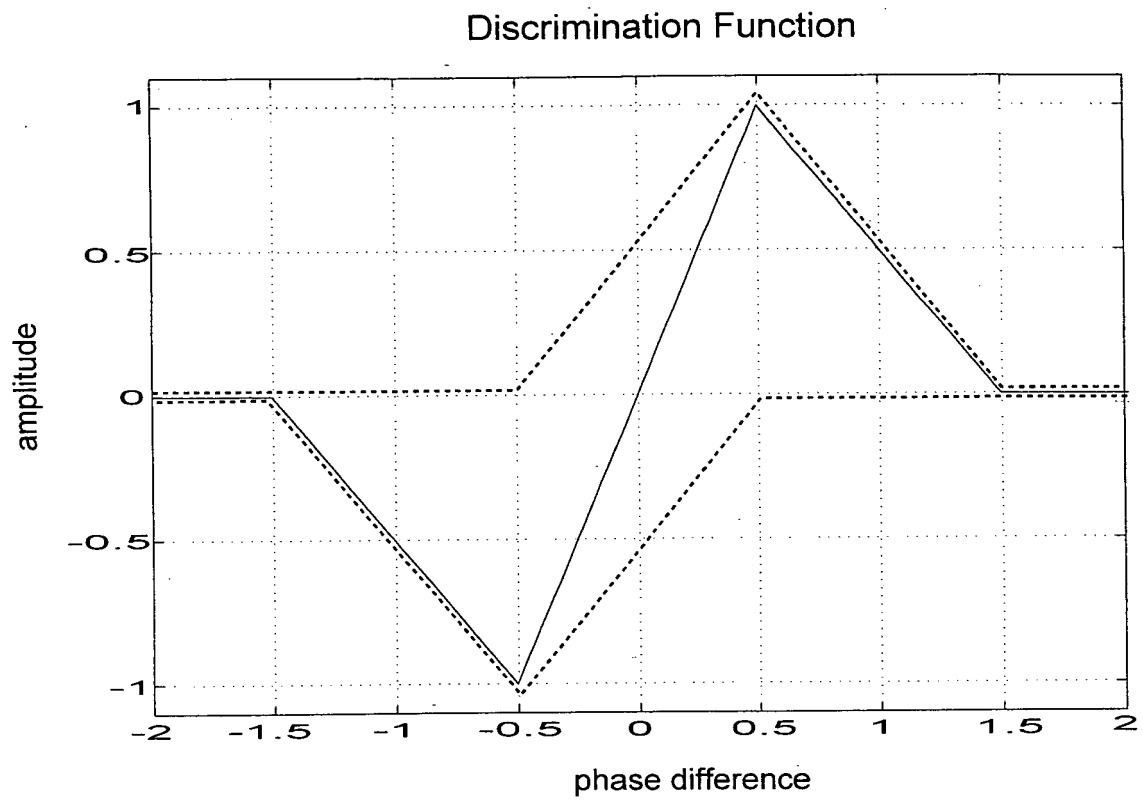


Fig 4A

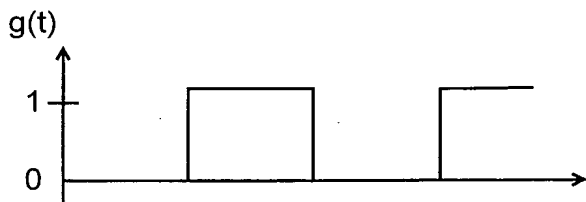
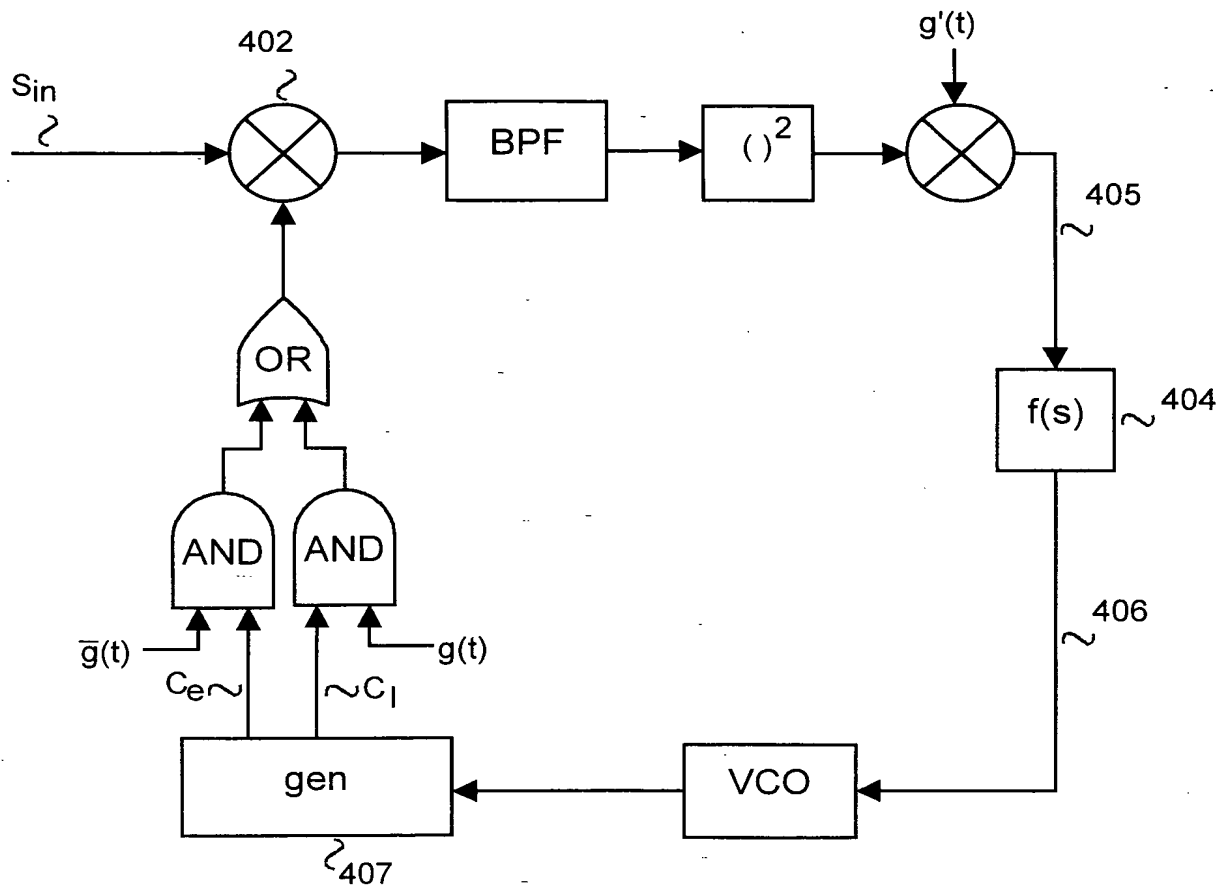


Fig 4B

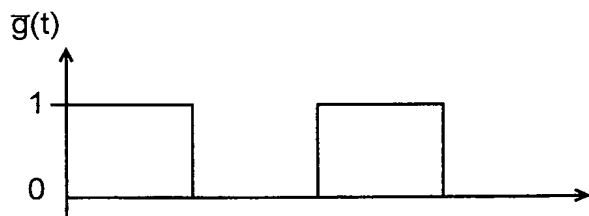


Fig 4C

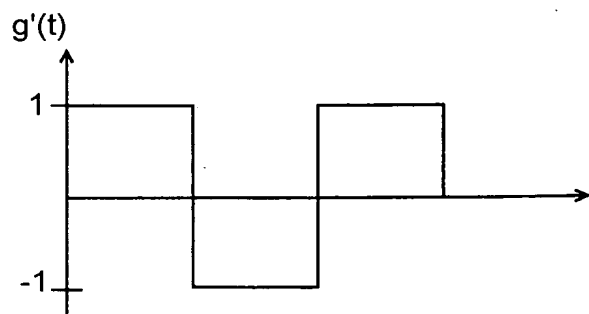


Fig 4D

**Fig 6**

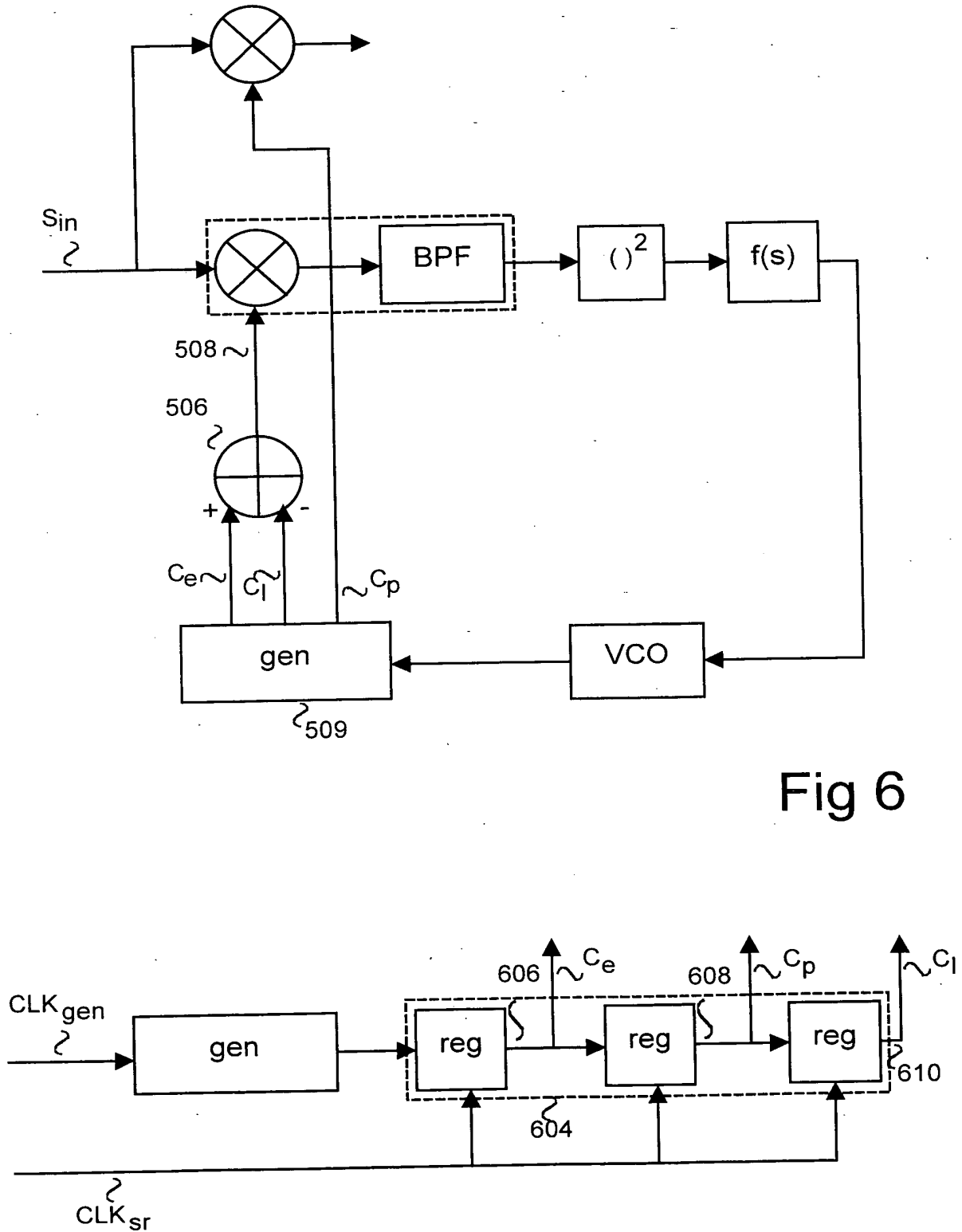


Fig 7

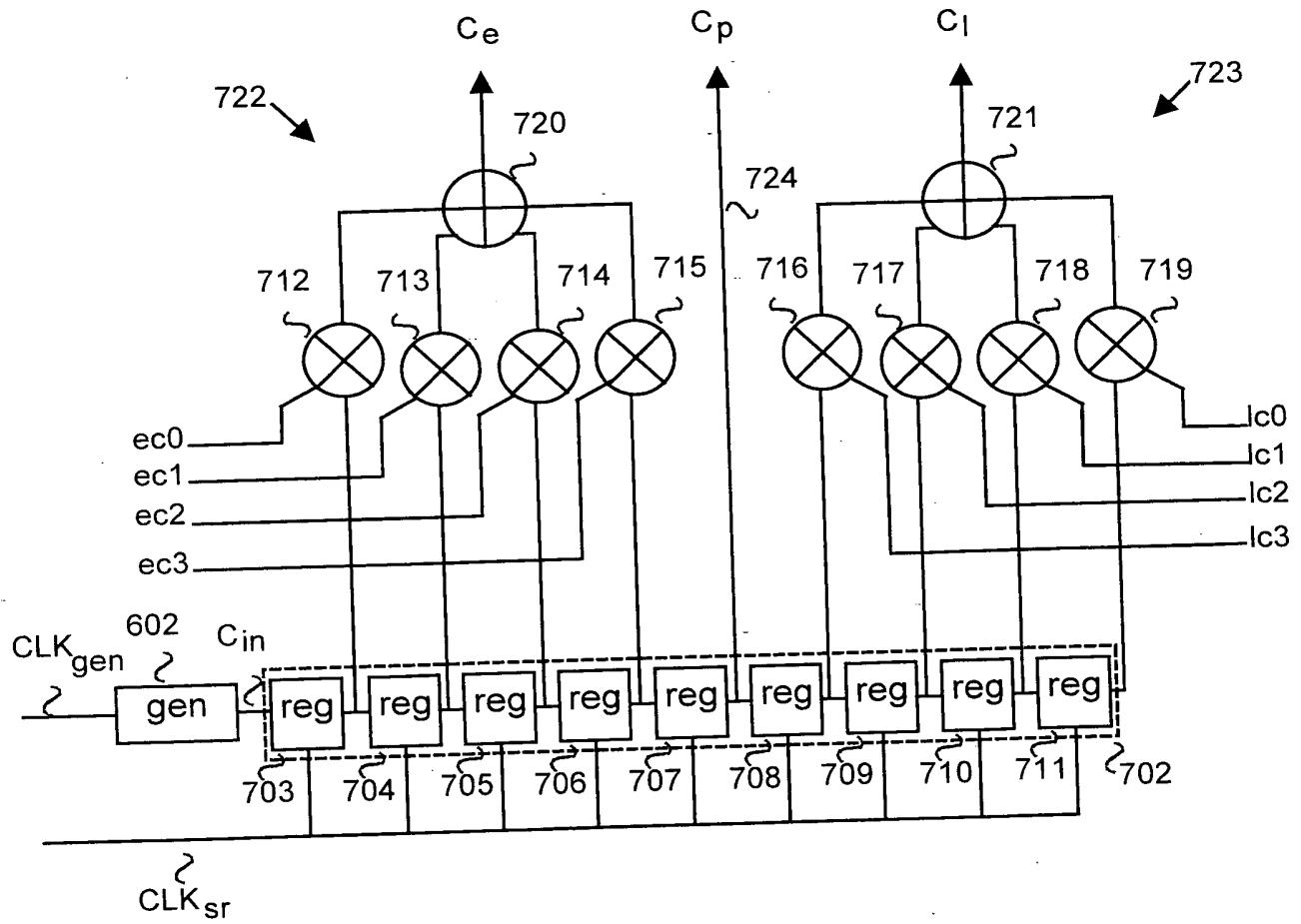


Fig 8

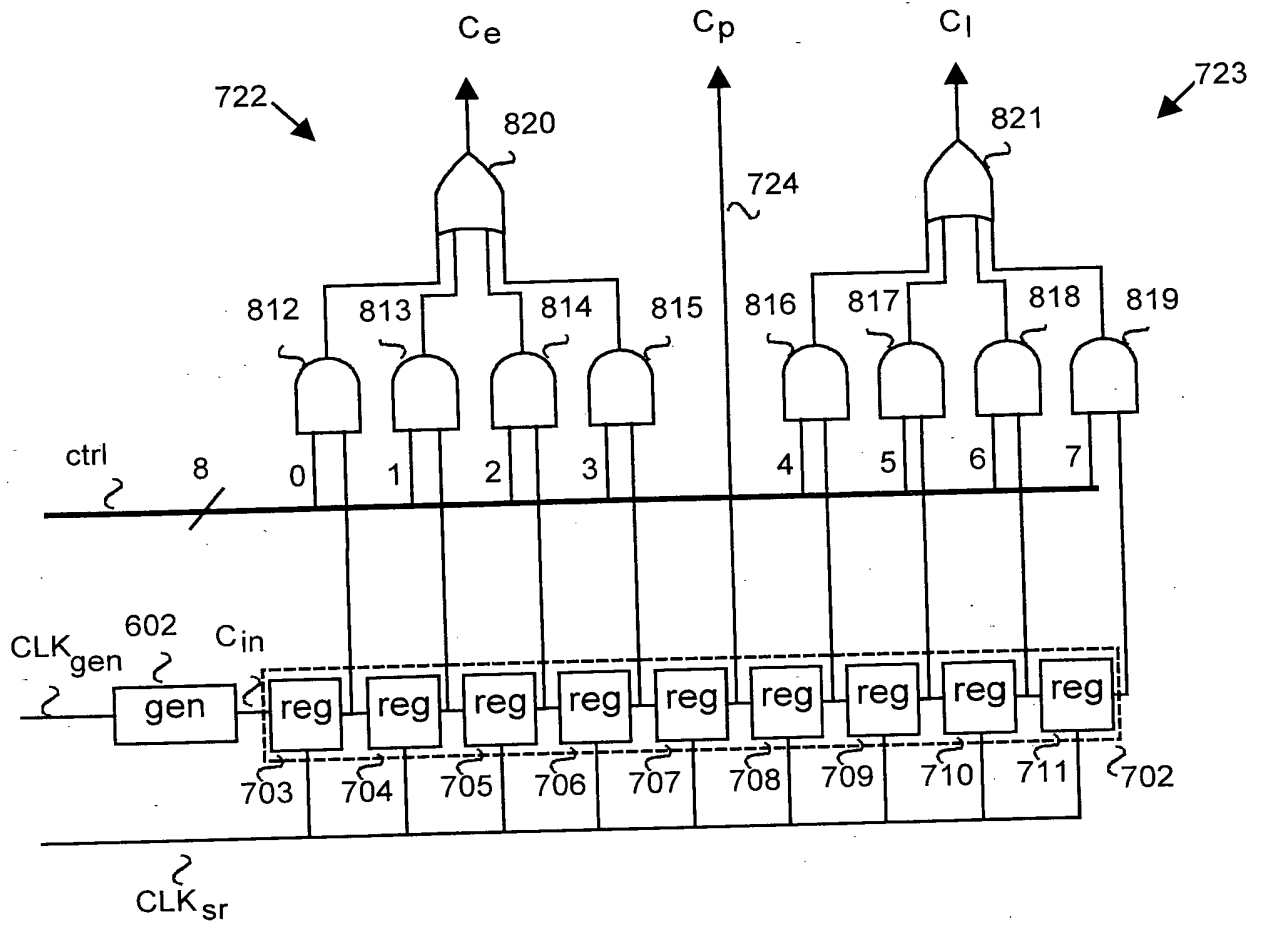


Fig 9A

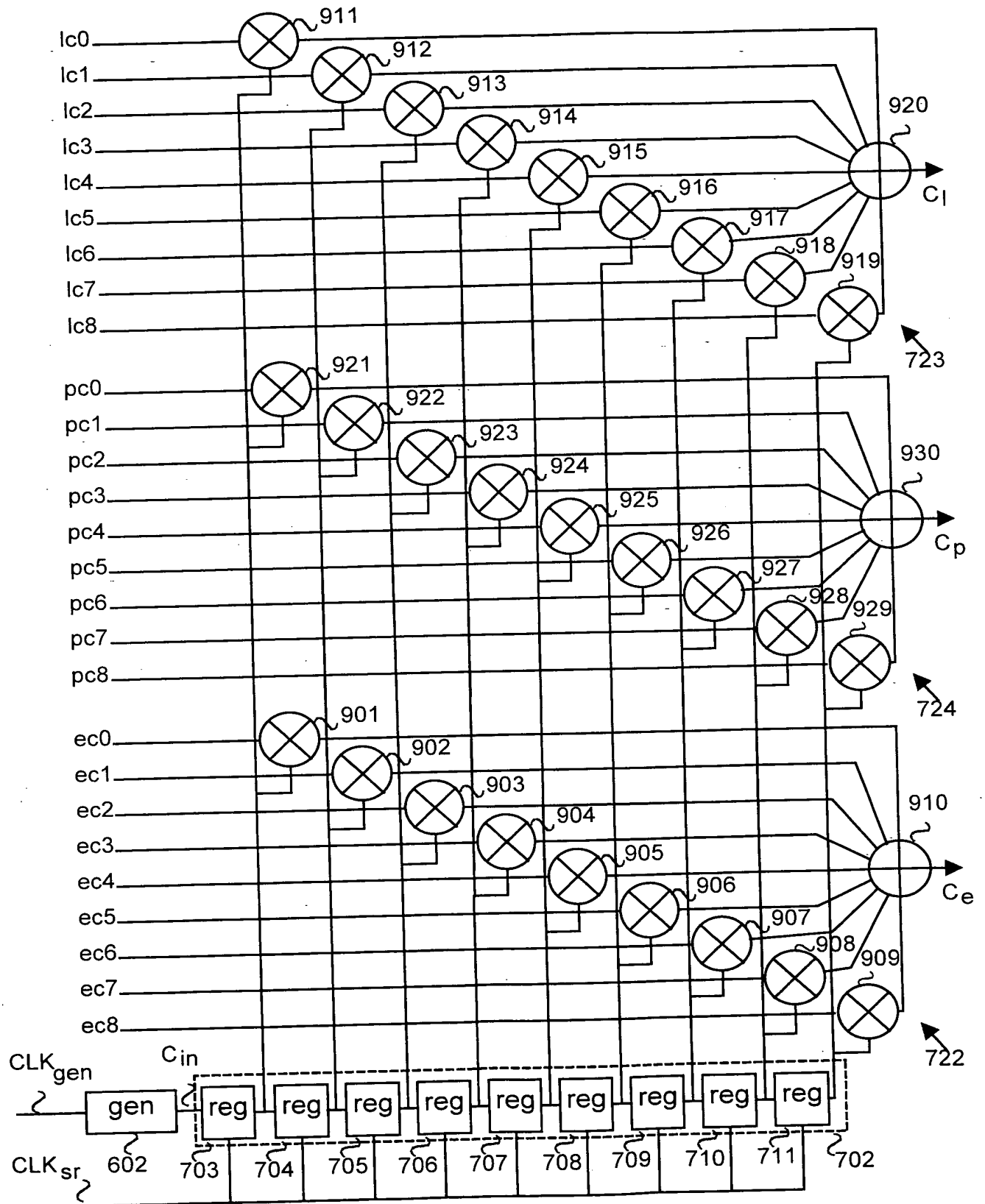




Fig 9B

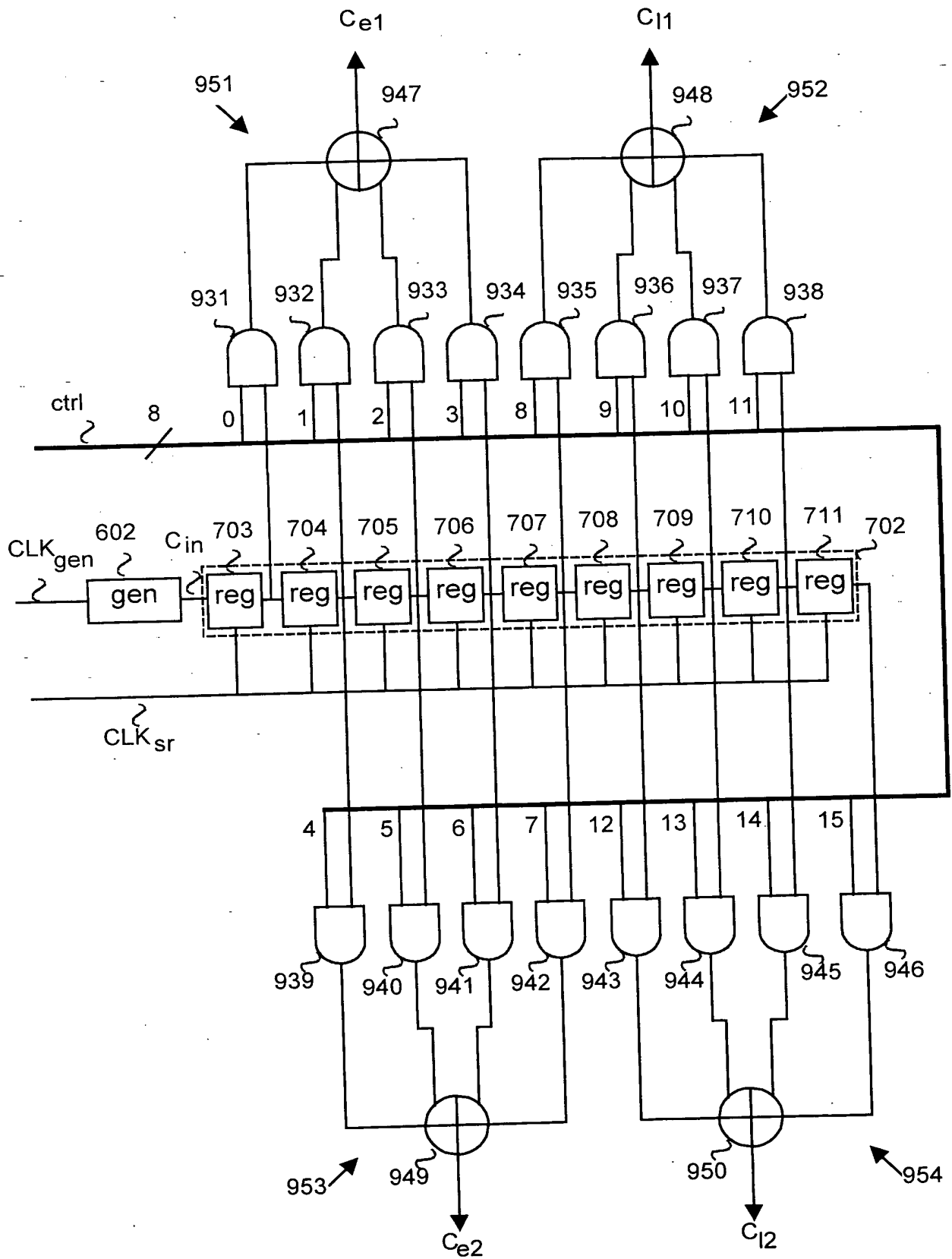


Fig 10A

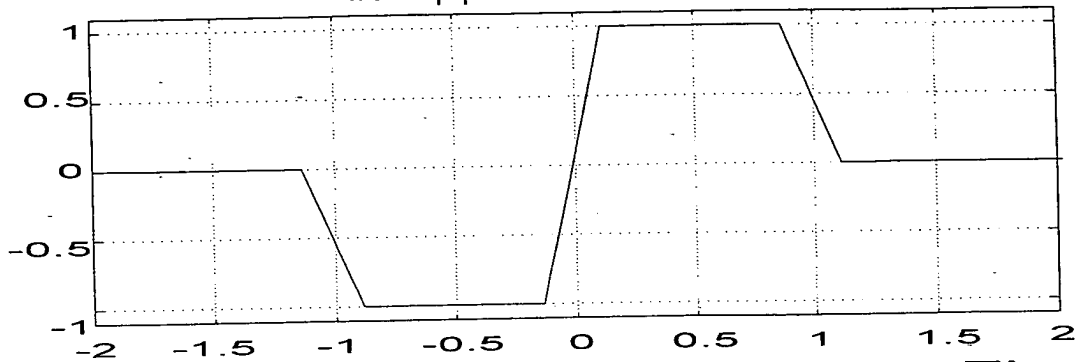
 $\pm 1/8$  chip phase difference; linear detection

Fig 10B

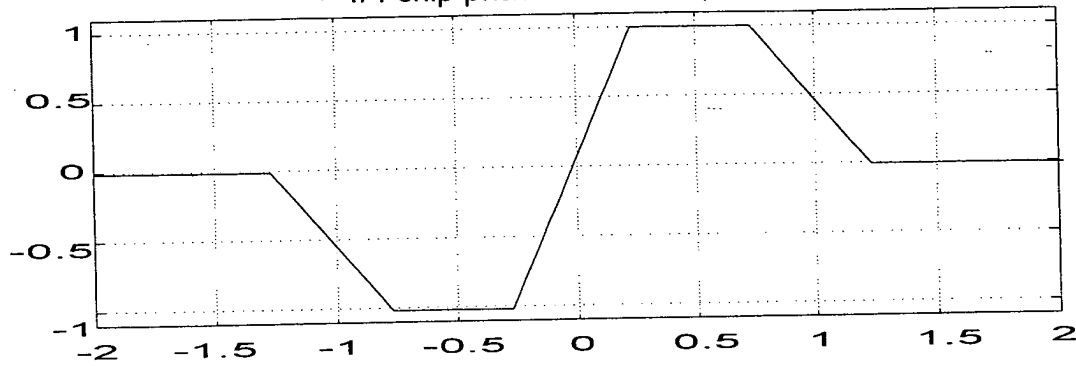
 $\pm 1/4$  chip phase difference; linear detection

Fig 10C

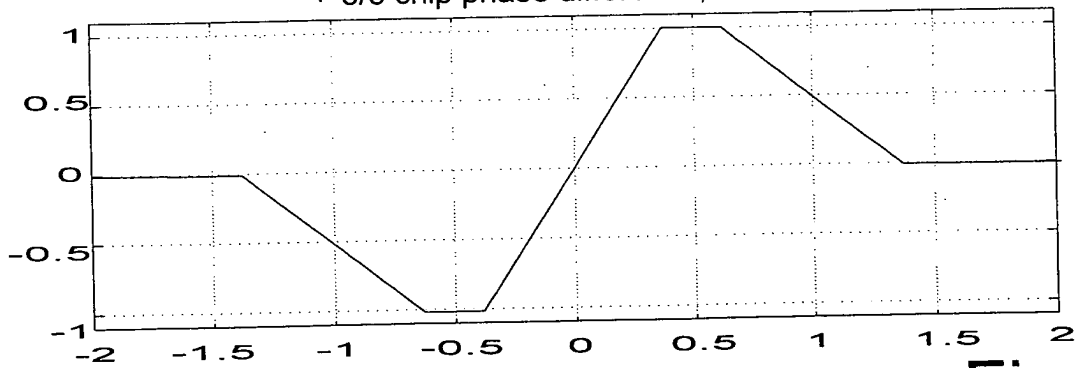
 $\pm 3/8$  chip phase difference; linear detection

Fig 10D

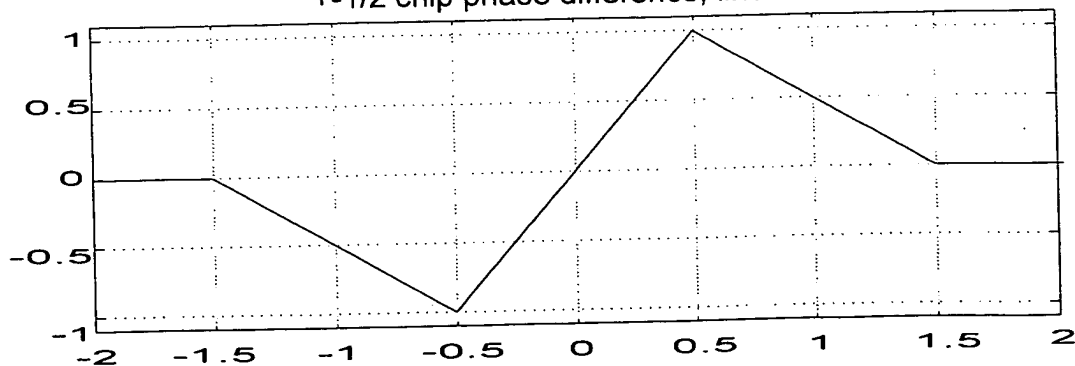
 $\pm 1/2$  chip phase difference; linear detection

Fig 11A

+1 chip phase difference; linear detection

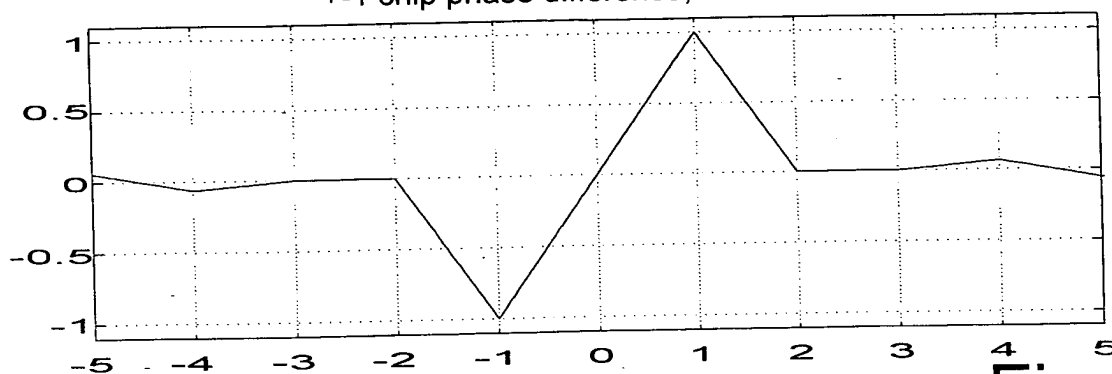


Fig 11B

+2 chip phase difference; linear detection

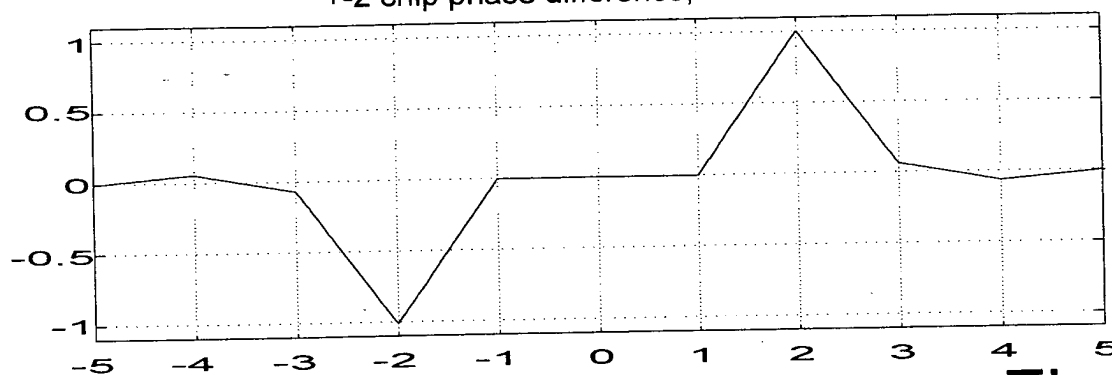


Fig 11C

sum of +1 - +4 chip phase differences; linear detection

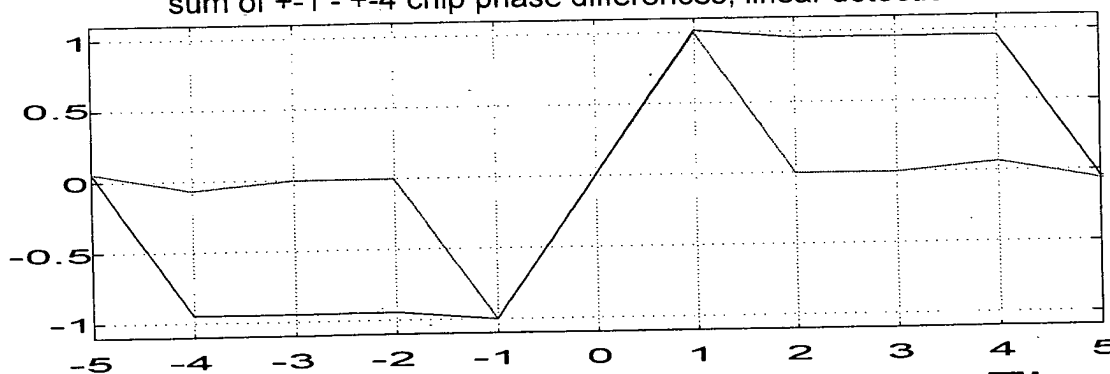


Fig 11D

linear combination of +1 - +4 chip phase differences; linear detection

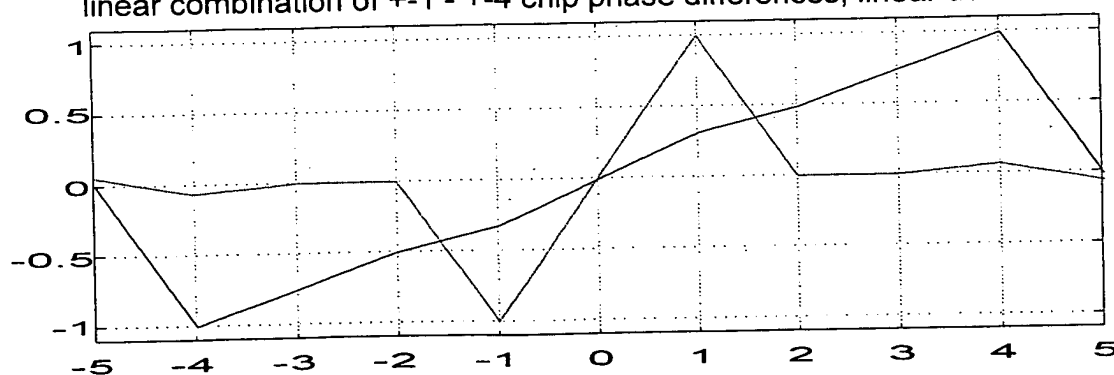


Fig 12A

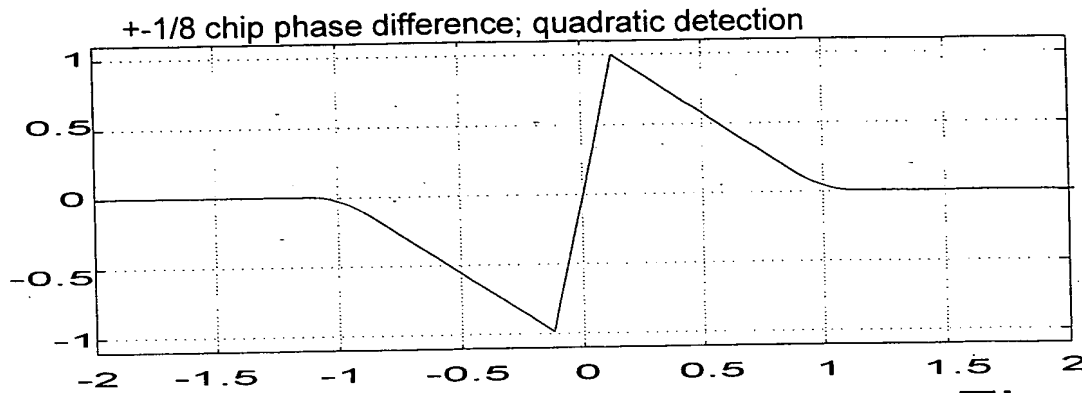


Fig 12B

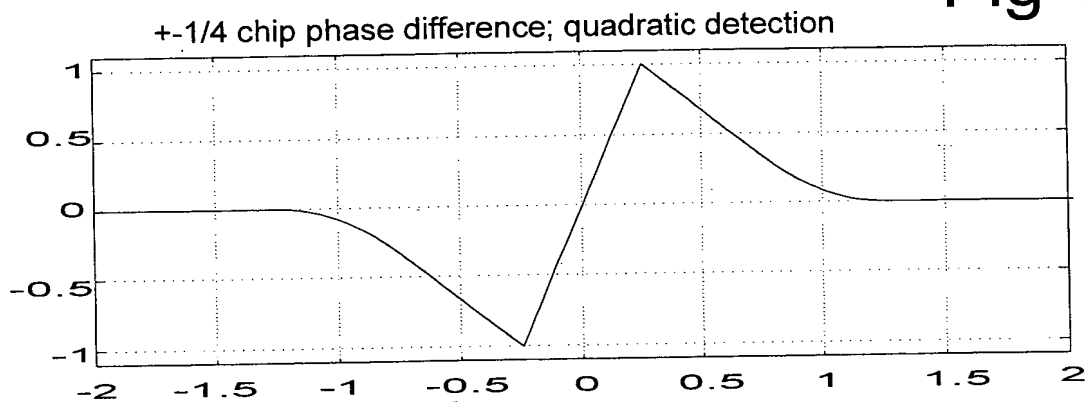


Fig 12C

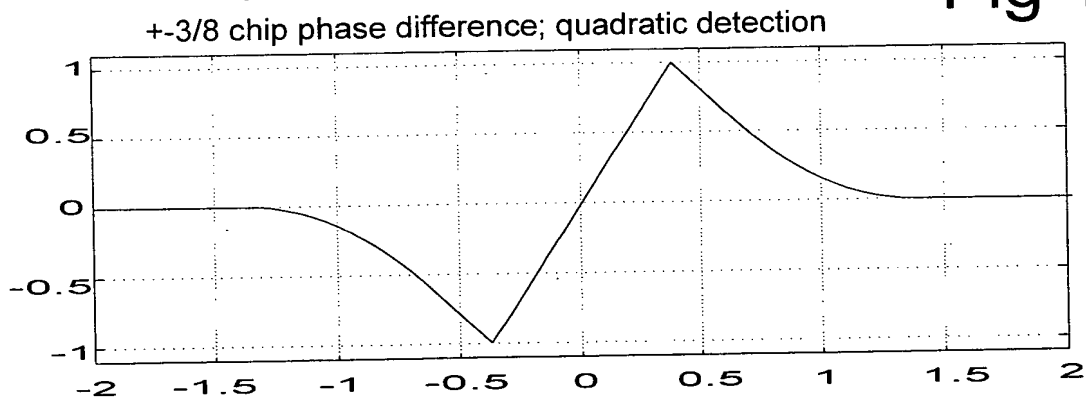


Fig 12D

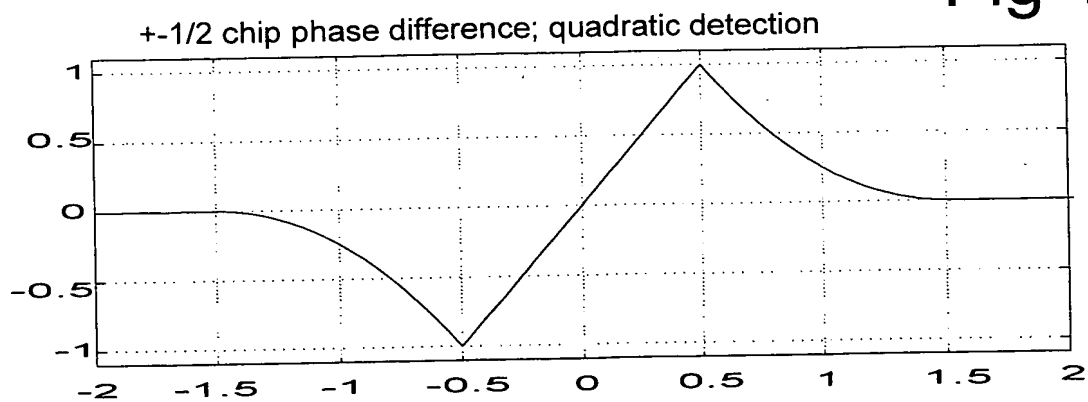


Fig 13A

$\pm 1/2$  chip phase difference; quadratic detection

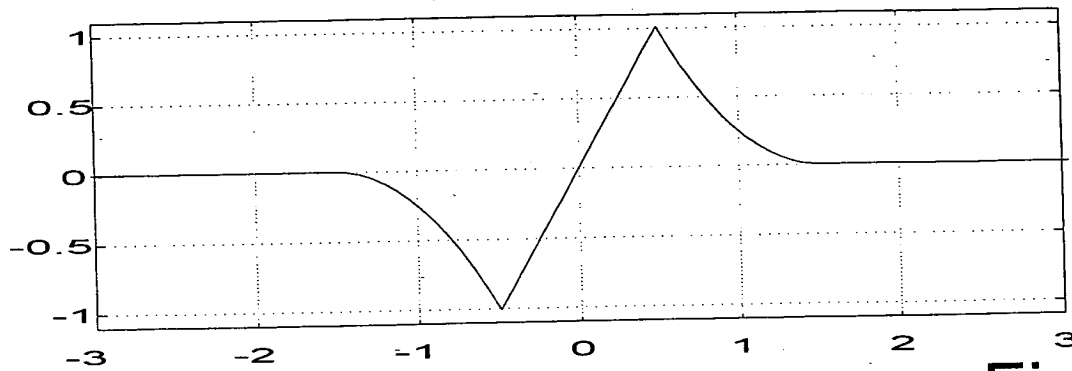


Fig 13B

$\pm 1$  chip phase difference; quadratic detection

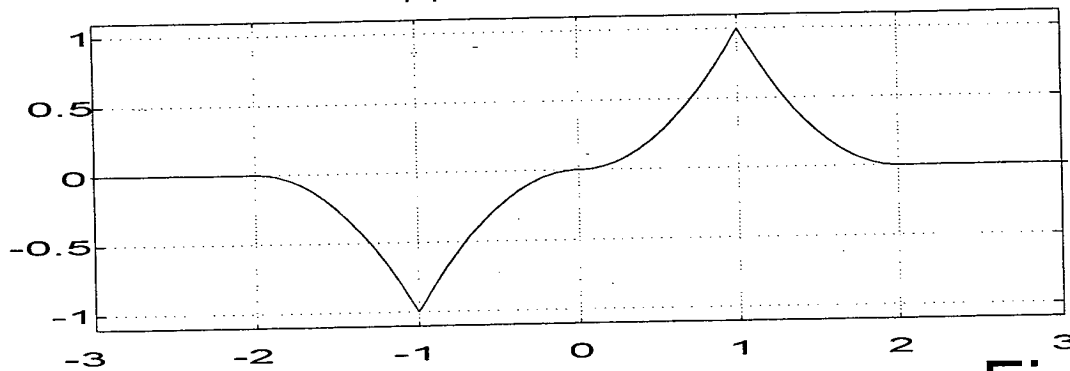


Fig 13C

sum of  $\pm 1/2$  -  $\pm 2$  chip phase differences; quadratic detection

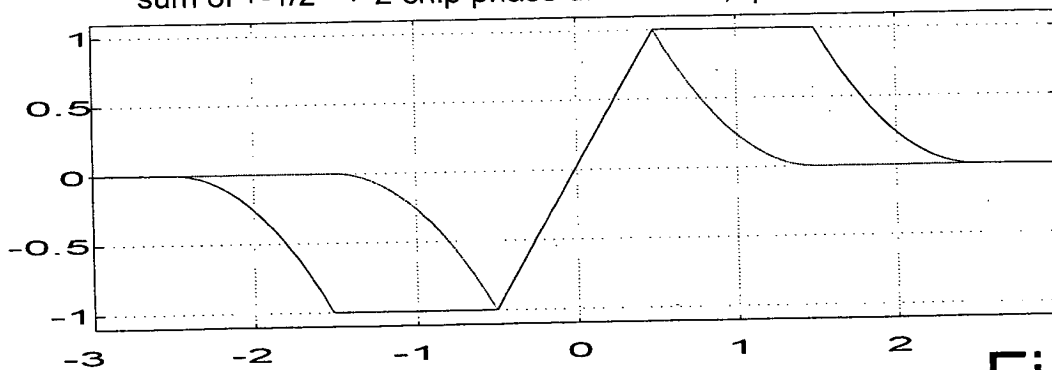


Fig 13D

linear combination of  $\pm 1/2$  -  $\pm 2$  chip phase differences; quadratic detection

